



fedora^f
electronic lab 10

Community Leader in opensource EDA deployment

About Us

Fedora Electronic Lab is a matured simulation platform for Micro Nano Electronics Engineering and Embedded Systems. FEL, a subset of Fedora, is available for free and has been already adopted by many universities and hobbyists. We believe in Reducing Deployment time, Design Focus and Increasing Productivity.

Design Tools for

Schematic Editors

gschem, xcircuit

Circuit/Spice Simulations

ngSpice, gnuicap, qucs

RTL Coding/Simulation

Icarus Verilog, GHDL

Layout, LVS, DRC Tools

Alliance, toped, Magic, netgen

Board Design

gEDA Toolset, gerbv

Waveform viewers

GTKWave, gWave

Cross Compilers

sdcc, avr gcc

μ P/ μ C Programming/Simulation

pikLab, gpsim, Ktechlab

And many more...

Design, Simulate & Program Electronics



WWW Home

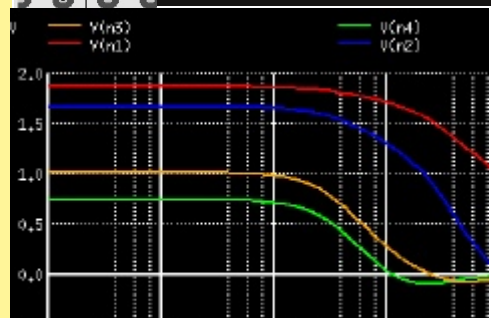
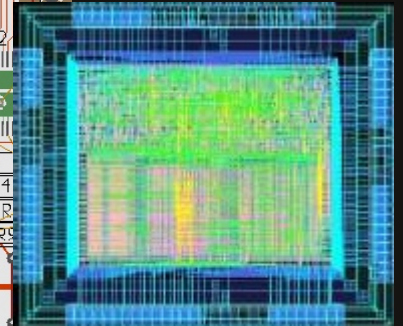
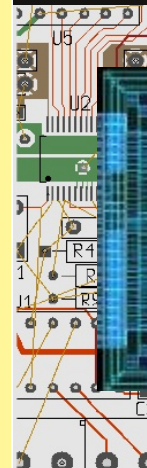
<http://chitlesh.fedorapeople.org/FEL/>

Mailing List

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Wiki HowTos

<https://fedorahosted.org/fedora-electronic-lab/wiki/HowTo>



The Fedora Project is maintained and driven by the community and sponsored by Red Hat. This is a community maintained document. Red Hat is not responsible for content.



ISO Download

<http://tnorth.ch/fel/F10-i686-Live-FEL.iso>

Torrent Download

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